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<u>L13</u>	L12 and l6	88	<u>L13</u>
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<u>L11</u>	L10 and (align\$8 or boundar\$4)	21564	<u>L11</u>
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Cheresiz, D.; Juurlink, B.; Vassiliadis, S.; Wijshoff, H.A.G.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on
Volume 13, Issue 1, Jan. 2005 Page(s):1 - 13

[AbstractPlus](#) | Full Text: [PDF\(768 KB\)](#) [IEEE JNL](#)**2. Access and alignment of arrays for a bidimensional parallel memory**

Verdier, C.; Boutillon, E.; Lafage, A.; Demeure, A.;
Application Specific Array Processors, 1994. Proceedings., International Conference on
22-24 Aug. 1994 Page(s):346 - 356

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